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Docket No.: 5016-0103PUS1

(Patent)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Tadahiro OHMI et al.

Application No.: 10/581,035

Confirmation No.: 5997

Filed:

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Art Unit:

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For:

THIN FILM TRANSISTOR INTEGRATED

Examiner:

M. CALEY

CIRCUIT DEVICE, ACTIVE MATRIX DISPLAY DEVICE, AND

MANUFACTURING METHODS OF THE

AMENDMENT AFTER FINAL OFFICE ACTION

MSAF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir

INTRODUCTORY COMMENTS

In reply to the Office Action dated August 30, 2010, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Claims begin on page 2.

Remarks begin on page 11.